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Lab 2 Report

ECE 2031 L07

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图片包含 日历

描述已自动生成

**Figure 1.** The K-map of a prototyping circuit with all the prime implicants circled on it and the result Boolean expression f = x · /z + x · y + w · y.

图示, 示意图

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**Figure 2.** Schematic of circuit implementing f = x · /z + x · y + w · y.

图片包含 游戏机, 电路

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**Figure 3.** Breadboard circuit implementing f = x · /z + x · y + w · y. The switch block controls the input signal, and the LED is an output indicator.

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**Figure 4.** The waveform of channel 1 and channel 2 captured by the oscilloscope.